Diffusion & Oxidation Doping



 $J = -D\frac{dn}{dx}$

Fick's First Law is used in steady state diffusion, i.e., when the concentration within the diffusion volume does not change with respect to time.

$$D = D_0 \exp\left(-\frac{E_{diff}}{k_B T}\right)$$

 E_{diff} : activation energy for diffusion

2nd Fick's law

$$J = -D\frac{dn}{dx}$$

Continuity :

Fick's Second Law is used in non-steady state diffusion, i.e., when the concentration within the diffusion volume changes in time.

$$dN = \left[J(x) - J(x + \Delta x)\right]Sdt = -\frac{dJ}{dx}dtdV$$
$$\frac{dn}{dt} = -\frac{dJ}{dx} \Rightarrow \frac{dn}{dt} = D\frac{\partial^2 n}{\partial x^2}$$

Constant source diffusion "pre-deposition"

$$n(x,t) = n_0 \operatorname{erfc}\left(\frac{x}{2\sqrt{Dt}}\right)$$

$$\operatorname{erfc}(x) = \frac{2}{\sqrt{\pi}} \int_{x}^{\infty} e^{-x^2} dx$$

(complimentary error function)



Limited source diffusion "drive-in"

$$n(x,t) = \frac{Q}{\sqrt{\pi Dt}} \exp\left(\frac{x}{2\sqrt{Dt}}\right)^2$$



Parameters that affect diffusion:

- Temperature
- Pressure
- Crystal direction (channeling)



Probability to escape in thermal activation process:

$$P = \omega \cdot an \cdot \exp\left(-\frac{W - eaE/2}{k_{\rm B}T}\right)$$

w- attempt frequency

Enhanced diffusion:

- Stress, electric field
- Ionization
- Grain boundaries (defects)



$$j = -D\frac{\mathrm{d}c}{\mathrm{d}x} \quad [D] = \frac{\mathrm{cm}^2}{\mathrm{s}}$$

	D [cm²/s]
H_2 in O_2	0.7
CO ₂ in air	0.14
O_2 in O_2	0.18
sugar in water	3e-6

In gases:

$$D \sim vl \sim \frac{v}{n\sigma} \sim \frac{v \, k_{\rm B}T}{P\sigma}$$

Time of equalization
$$t \sim \frac{L^2}{D}$$

u = BF

Mobility *B*, force *F*, drift velocity *u* of ions

 $D = k_B T B$ Einstein's equation





n - TYPE ENHANCEMENT-MODE MOS TRANSISTOR





Mechanism of Diffusion



- a) substitutional diffusion
- b) impurity atom replaces Si atom
- c) impurity atom does not replace Si atom

Doping





(a)

(b)



Figure 6-11 (a) Schematic representation of hot-wall, multiple wafer LPCVD reactor geometry with gas flow boundary conditions. (b) Film thickness variation as a function of the scaled radial distance along the wafer for different values of ϕ . (From *Chemical Vapor Deposition*, edited by M. L. Hitchman and K. F. Jensen. Reprinted with the permission of Academic Press, Ltd., and Professor K. F. Jensen, MIT.)

 $2B_{2}O_{3} + 3Si \rightarrow 4B + 3SiO_{2}$ $4BBr_{3} + 3O_{2} \rightarrow 2B_{2}O_{3} + 6Br_{2}$ $B_{2}H_{6} + 3O_{2} \rightarrow B_{2}O_{3} + 3H_{2}O$

 $2P_2O_5 + 5Si \rightarrow 4P + 5SiO_2$ $4POCl_3 + 3O_2 \rightarrow 2P_2O_5 + 6Cl_2$ $2PH_3 + 4O_2 \rightarrow P_2O_5 + 3H_2O$



Doping • by thermal diffusion



Tool #433 Rapid Thermal Processor (RTP) AG Heatpulse 610

a PC-controlled halogen lamp furnace. It accommodates 2", 3", and 4" wafers, or small pieces placed on a wafer.

It is typically used for alloying of ohmic contacts on III-V semiconductors. No other substrates than III-V are allowed.

Specifications: Protecting gas: N2 Wafer size: pieces, 2", 3", 4" Max temperature:1000°C Heating rate: 120°C/sec Temperature sensor: Thermocouple / Pyrometer



diffusion of dopant atoms in the direction parallel to the surface of semiconductor; undesired in device manufacturing as it causes distortion of the device geometry.

Thermal Oxidation of Si



FIGURE 1.12. Thermal oxidation of SiO₂ layer. From Ref. 30.

 $Si + O_2 \rightarrow SiO_2$ $Si + 2H_2O \rightarrow SiO_2 + 2H_2$

- 1. Oxygen is transported from the bulk of the gas phase to the gas-oxide interface
- 2. Oxygen diffuses through the growing solid oxide film
- 3. When oxygen reaches the Si/SiO₂ interface, it chemically reacts with Si and forms SiO₂



Figure 6-12 Model of CVD growth process. Gas flows normal to plane of paper. (From Ref. 26. Reprinted with permission from John Wiley & Sons, copyright © 1967.)

$$J_{gs} = h_g (C_g - C_s) \quad J_s = k_s C_s$$

$$C_s = \frac{C_g}{1 + k_s / h_g} \text{ for } J_{gs} = J_s$$

$$k_s \propto \exp\left(-\frac{E}{RT}\right) \quad \substack{k_s >> h_g: \text{ mass transfer} \\ k_s << h_g: \text{ surface reaction}}$$



Figure 6-13 Deposition rate of Si from four different precursor gases as a function of temperature. (From W. Kern, in *Microelectronic Materials and Processes*, ed. R. A. Levy, reprinted with permission of Kluwer Academic Publishers, 1989.)

$$J_1 = h_g \left(C_g - C_0 \right)$$
$$J_2 = \frac{D(C_0 - C_i)}{d_0}$$

 $J_3 = k_S C_i$

 C_g – concentration of oxygen in the gas C_i – concentration of oxygen at the gas/SiO₂ interface C_0 – concentration of oxygen at the SiO₂ / Si interface h_g – mass transport coefficient D – diff. coeff. of oxygen in SiO₂ k_s – chemical reaction rate constant

$$J_1 = J_2 = J_3$$
 (steady-state growth)





$$C_i \approx C_0 = \frac{C_g}{1 + \frac{k_s}{h_G}}$$
 reaction limited $(D >> k_s d_0)$

 $C_0 \approx C_g; C_i \approx 0$: diffusion limited $(D \ll k_S d_0)$

the actual growth depends on J3:

$$\frac{d}{dt}d_0(t) = \frac{k_s C_i}{N_0}$$
$$d_0^2 + Ad_0 = B(t+\tau)$$

the actual growth depends on J3:

$$\frac{d}{dt}d_{0}(t) = \frac{k_{s}C_{i}}{N_{0}}$$

$$d_{0}^{2} + Ad_{0} = B(t+\tau)$$

$$N_{0} = 2.2 \times 10^{22} \text{ (dry) } 4.4 \times 10^{22} \text{ (wet) } [\text{cm}^{-3}]$$

$$A = 2D(h_{g}^{-1} + k_{s}^{-1}); \quad B = 2DC_{g} / N_{0}; \quad \tau = (d_{i}^{2} + Ad_{i}) / B$$

$$d_{i} - \text{initial oxide thickness (if any)}$$

$$d_0(t) = \frac{A}{2} \sqrt{1 + \frac{t+\tau}{A^2/4B}} - 1$$

$$d_0(t) \cong Bt, \quad t \gg A^2/4B$$

$$d_0(t) \cong \frac{B}{A}(t+\tau), \quad t+\tau \ll A^2/4B$$



Oxide Expansion

- Cracks lower breakdown voltage (5-10 MV/cm
- Leakage
- Trapped charges



Cross section depicting process sequence for local oxidation of silicon (LOCOS): (a) semirecessed and (b) fully recessed structures.

Mechanical Relaxation Effects during oxide growth (expansion)

Stress relaxation:

Strain relaxation:



Diffusion Along Grain Boundaries



http://shasta.mpi-stuttgart.mpg.de/research/thinfilmcnt/thinfilms.htm

Interdiffusion & Transformations in Thin Films



Interdiffusion is crucial in multilayers

MoSi multilayers **Electro-migration**



•EUV lithography •EUV metrology •EUV microscopy •synchrotron optics •x-ray astronomy •soft x-ray lasers •element analysis •plasma physics

http://www.iof.fraunhofer.de/departments/optical-coatings/

50 nm

M. Ohring: Materials Science of thin films

Si Crystal Structure

diamond structure



Two interleaving FCC cells offset by 1/4 of the cube diagonal





Primary and secondary flats on silicon wafers.

Isotropic vs Anisotropic Wet Etching





solution agitation.

Chemicals

```
<u>Isotropic</u>
HF:HNO<sub>3</sub>:CH<sub>3</sub>COOH:H<sub>2</sub>0
HF
HF:NH<sub>4</sub>F
```

<u>Masking Materials</u> Photoresist (Acids Only) Si₃N₄ SiO₂

Anisotropic KOH EDP (Ethylenediamine Pyrocatechol) CsOH NaOH $N_2H_4-H_20$ (Hydrazine)

Chemicals

<u>KOH Etching</u> Etch rate

(110) > (100) > (111) (111) > (110) > (111) w/ IPA

Varies with T and concentration



The SiO₂ etch rate in nm/hr as a function of KOH concentration at 60°C. (From Seidel, H. et al., *J. Electrochem. Soc.*, 137, 3612-3626, 1990. With permission.)

<u>Masks</u> Si₃N₄: is the best, very slow etch rate, selectivity > 1000 SiO₂ :selectivity >> 100

Application of Anisotropic Etch

Orifice (A via through Si wafer)



Etch Stop Layers

Boron Stops Etching ~10²⁰ cm⁻³ reduces etch rate 1000 times



Relative etch rate for (100) Si in EDP and KOH solutions as a function of concentration of boron, phosphorus, and germanium. (From Seidel, H. et al., *J. Electrochem. Soc.*, 137, 3626–3632, 1990. With permission.)



Boron-doped Layer

Laser-Assisted Wet Etching

A

Fundamentals of Microfabrication Laser light damaged or 'spoiled' zone Masking layer {111} <110> Si В 70.53 a 54,74 70,53

M. Madou, Fundamentals of Microfabrication

Laser light

Macroporous Si



www-tkm.physik.uni-karlsruhe.de/

http://www.macroporous-silicon.com/



Photogenerated minority carriers (in the case of n-type Si this means "holes") diffuse from the back side of the sample to the pore (etch) pits and promote dissolution there, because of the enhanced electrical field in the space charge layer (SCL).